

Prof. (Dr.) Sachchidanand Shukla

Vice Chancellor, Pt. Ravishankar Shukla University, Raipur, Chhattisgarh - 492010, INDIA

A BRIEF SKETCH

- **Trained for Academic Administration under MHRD's LEAP program** at Tata Institute of Social Sciences, Mumbai and University of Pennsylvania, USA
- Visited **07 Countries** for Research Training & Presentation (USA, Italy, Malaysia, Mauritius, Thailand, Sri Lanka and Nepal)
- **Member**, Raja Rammohun Roy Library Foundation, Kolkata (Ministry of Culture, Govt of India)
- **Member**, UGC Central Zonal Committee for the Implementation of NEP-2020
- **General Secretary**, International Academy of Physical Sciences (IAPS)

Academic Administration

- **Vice Chancellor**, Pt. Ravishankar Shukla University, Raipur, Chhattisgarh
- Former **Pro Vice Chancellor**, Dr. R.M.L. Avadh University, Ayodhya, UP
- Former **Registrar**, Dr. R.M.L. Avadh University, Ayodhya
- Member, UGC Central Zonal Committee for the Implementation of NEP-2020
- Convener, NEP Task Force, Higher Education Department, Chhattisgarh
- Former Head, Department of Physics & Electronics, Dr. R.M.L. Avadh University, Ayodhya
- Senate Member, I.I.T., Bhilai
- Governor's nominee, Board of Management, Mohan Lal Sukhadia University, Udaipur, Rajasthan
- Governor's nominee (Former), Board of Management, R.R.B.M. University, Alwar, Rajasthan
- Governor's nominee, Selection Committee, University of Rajasthan, Jaipur, Rajasthan
- Governor's nominee, Selection Committee, Dr. Bhimrao Ambedkar Law University, Jaipur
- Governor's nominee, Selection Committee, Govind Guru Tribal University, Banswara
- Former Director, College Development Council (CDC), Dr. R.M.L. Avadh University, Ayodhya
- Former President, Sports Council, Dr. R.M.L. Avadh University, Ayodhya
- Former Coordinator, Department of Mass Communication & Journalism, R.M.L. A.U., Ayodhya
- Director, Teachers Training Cell, Dr. R.M.L. Avadh University, Ayodhya
- Member, Executive Council, Dr. R.M.L. Avadh University, Ayodhya
- Member, Academic Council, Dr. R.M.L. Avadh University, Ayodhya
- Former Coordinator, UGC-Cell, Dr. R.M.L. Avadh University, Ayodhya
- Former Coordinator, RUSA Cell, Dr. R.M.L. Avadh University, Ayodhya

Academic Contribution

- Books Authored – 07, Books Edited – 01
- Research Papers Published – 122
 - Research Papers Published in International/National Journals – 95
 - Research Papers Published in International/National Conference Proceedings – 18
 - Research Papers Published as Book's Chapter – 09
- General Articles Published in News Paper – 18
- Research (PhD) Supervision – 19 (PhD Awarded – 14, PhD Enrolled – 05)
- Current Research Projects – 02 (As PI – 01, As Co-PI – 01)
- Consultancy Services – 02 (For Tata Consultancy Services Ltd)
- Invited Lectures in Conferences / Webinars / FDP – 36
- Research Paper presented in International/National Conferences – 30
- Awards/Recognition – 17
- Journal Editorial Board / Peer Reviewer – 10
- Association with Academic Bodies / Committees – 20
- Participation in Training Programs/ Refresher Courses – 05
- Conference / Workshop Organized – 04

Prof. (Dr.) Sachchidanand Shukla

(Fellow IACSIT, Fellow SASS, Associate Fellow IAPS)

Vice Chancellor

Pt. Ravishankar Shukla University, Raipur, Chhattisgarh - 492010, INDIA

CURRICULUM VITAE

Name: Dr. Sachchidanand Shukla

Present Designation: Vice Chancellor, Pt. Ravishankar Shukla University, Raipur, Chhattisgarh

Past Designation: Professor (On Lien), Department of Physics and Electronics, Dr. Ram Manohar Lohia Avadh University, Ayodhya, UP

Bate of Birth: January 20, 1967

E-Mail: sachida.shukla@gmail.com, sachida.shukla@rmlau.ac.in

TRAINING FOR ACADEMIC LEADERSHIP

Trained for Academic Administration under Ministry of Human Resources and Development, Government of India's **Leadership for Academicians Program (LEAP)** jointly at **Tata Institute of Social Sciences, Mumbai** and **University of Pennsylvania, USA in 2019.**

ADMINISTRATIVE EXPERIENCE

1. **Vice Chancellor**, Pt. Ravishankar Shukla University, Raipur, Chhattisgarh (April 01, 2023 till date)
2. **Pro Vice Chancellor**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (November 29, 2018 to July 31, 2020)
3. **Registrar**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (June 29, 2018 to October 12, 2018)
4. **Member**, UGC Central Zonal Committee for the Implementation of NEP-2020 (02 years from September 13, 2024)
5. **Member**, Raja Rammohun Roy Library Foundation, Kolkata (Ministry of Culture, Govt. of India) (03 years from April 1, 2024)
6. **Convener**, NEP Task Force, Higher Education Department, Chhattisgarh (Letter dtd. April 15, 2024)
7. **Governor's nominee**, Board of Management, Mohan Lal Sukhadia University, Udaipur, Rajasthan (for 01 year from July 28, 2023)
8. **Governor's nominee**, Board of Management, Raj Rishi Bhartrihari Matsya University, Alwar, Rajasthan (May 15, 2020 to May 14, 2023)
9. **Governor's nominee**, Selection Committee, University of Rajasthan, Jaipur, Rajasthan (for 04 years from February 17, 2021)
10. **Governor's nominee**, Selection Committee, Dr. Bhimrao Ambedkar Law University, Jaipur, Rajasthan (for 02 years from May 26, 2022 and for 01 year from June 07, 2024)

11. **Governor's nominee**, Selection Committee, Govind Guru Tribal University, Banswara (for 01 year from August 03, 2022)
12. **Senate Member**, I.I.T., Bhilai (May 08, 2024 till date)
13. **Member**, Stirring Committee for SET-2024, conducted by Chhattisgarh Vyavsaik Pariksha Mandal (VYAPAM) (January 29, 2024 till date)
14. **Member**, Selection Committee for Physics, U.P. Higher Education Services Commission, Prayagraj (June 09, 2022)
15. **Director, College Development Council (CDC)**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (October 30, 2017 to October 30, 2019)
16. **Head, Department of Physics and Electronics**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (July 19, 2014 to July 19, 2017)
17. **President, Sports Council**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (August 14, 2010 to August 14, 2012)
18. **Member, Executive-Council**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (April 01, 2022 to March 31, 2023; June 29, 2018 to July 31, 2020; June 22, 2012 to June 21, 2013 and October 10, 2001 to October 9, 2002)
19. **Coordinator, NEP Task Force**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (February 22, 2021 to March 31, 2023)
20. **Director, Alumni Cell**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (February 11, 2021 to March 31, 2023)
21. **Director, Teacher's Training Cell**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (February 11, 2021 to March 31, 2023)
22. **Coordinator, RUSA Cell**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (April 6, 2017 to December 24, 2018)
23. **Coordinator, Department of Mass Communication & Journalism**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (April 06, 2017 to April 30, 2018)
24. **Coordinator UGC-Cell**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (March 13, 2015 to March 28, 2017)
25. **Media-in-charge**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (July 14, 2003 to April 30, 2018)
26. **In-charge, Engineering Section**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (May 2, 2016 to June 27, 2017)
27. **Member, Purchase Committee**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (April 26, 2017 to June 29, 2018)
28. **Member, IQAC, (Internal Quality Assurance Cell)** Dr. Ram Manohar Lohia Avadh University, Ayodhya (April 10, 2015 till date)
29. **Assistant Dean Student Welfare (ADSW)**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (September 1998 to December 2002)
30. **Coordinator, B.Tech. Programs, Institute of Engineering and Technology**, Dr. Ram Manohar Lohia Avadh University, Ayodhya (April 7 to September 30, 2001)

31. Member, NSS Advisory Board, Dr. Ram Manohar Lohia Avadh University, Ayodhya (Sessions 2011-12, 2012-13, 2017-18, 2019-20)

ACADEMIC VISITS (ABROAD)

1. **MHRD's Leadership for Academicians Program (LEAP)** jointly at **Tata Institute of Social Sciences, Mumbai** (August 18-29, 2019) and at **University of Pennsylvania, USA** (September 16-20, 2019)
2. 23rd International Conference of International Academy of Physical Sciences (CONIAPS XXIII), November 16-18, 2018, **Nepal Academy of Science & Technology (NAST)**, Kathmandu, **NEPAL**
3. Conference on Science and Technology (CST-2018), Mahendranagar, Kanchanpur, Province-7, **NEPAL**, May 21-22, 2018 (Organized by NAST, Nepal)
4. Asian Regional Conference and Workshop on Advanced Reconfigurable Instrumentation for Scientific Applications, **Universiti Kebangsaan Malaysia**, Bangi, Selangor, **MALAYSIA**, November 14-25, 2016 (organized by ICTP, Italy)
5. IEEE Radio and Antenna Days of the Indian Ocean (IEEE RADIO 2015), September 21-24, 2015, Belle Mare, **MAURITIUS**
6. 11th IEEE International Conference on Semiconductor Electronics (IEEE ICSE 2014), August 27-29, 2014, Kuala Lumpur, **MALAYSIA**
7. International Training Workshop on FPGA Design for Scientific Instrumentation and Computing, November 11-22, 2013 at **International Center for Theoretical Physics (ICTP)**, **Trieste, ITALY**.
8. 2nd International Conference on Engineering Mathematics and Physics' (ICEMP 2013), June 15-16, 2013, Colombo, **SRI LANKA**
9. 4th International Conference on Computer Modeling and Simulation (ICCMS 2013), February 24-25, 2013, Rome, **ITALY**
10. 15th International Conference of International Academy of Physical Sciences (CONIAPS XV), December 9-13, 2012, **Rajamangala University of Technology, Thanyaburi, THAILAND**
11. 10th IEEE International Conference on Semiconductor Electronics (IEEE ICSE 2012), September 19-21, 2012, Kuala Lumpur, **MALAYSIA**

ASSOCIATION WITH ACADEMIC BODIES / COMMITTEES (22)

1. **Fellow, SASS** (*Scholars Academic & Scientific Society*) 2020
2. **Fellow, IETE** (*Institution of Electronics & Telecommunication Engineers*) 2019
3. **Fellow, IACSIT** (*International Association of Computer Science and Information Technology, Singapore*) 2018
4. **Associate Fellow, IAPS** (*International Academy of Physical Sciences*) 2018
5. **General Secretary, IAPS** (*International Academy of Physical Sciences, Allahabad, India*), Membership No.-N-12205

6. Life-Member, ISCA (*The Indian Science Congress Association, Kolkata, India*), Membership No.-L-28293
7. Member, IEEE (*Institute of Electrical and Electronics Engineers, USA*), Membership No.-93575355
8. Senior-Member, SCIEI (*Science and Engineering Institute, Hong Kong, SAR of China*, Membership No.-2013073001
9. Life-Member, NASI (*The National Academy of Sciences, India, Allahabad, India*)
10. Life-Member, IETE (*The Institution of Electronics and Telecommunication Engineers, New Delhi, India*), Membership No.-M-203187
11. Member, Research Development Council, Ayodhya Research Institute (An Autonomous Research Center of Ministry of Culture, UP Government) (October 01, 2015 till date)
12. Member, Departmental Research Committee (DRC), Guru Ghasidas Vishwavidyalaya, Bilaspur, Chhattisgarh (May 10, 2023 till date)
13. Member, Academic-Council, Dr. RamManohar Lohia Avadh University, Ayodhya (July 2002 to June 2005 and July 19, 2014 to March 31, 2023)
14. Member, Academic-Council, Kisan Post Graduate College (An Autonomous College), Bahraich, UP (February 21, 2023 till Date)
15. Convener, Research Development Committee (RDC) of Physics & Electronics, Dr. RamManohar Lohia Avadh University, Ayodhya (July 19, 2014 to July 19, 2017)
16. Convener Board of Studies (BOS) of M.Sc.-Physics & M.Sc.-Electronics, Dr. RamManohar Lohia Avadh University, Ayodhya (July 19, 2014 to July 19, 2017)
17. Member, Board of Studies (BOS) of Physics, **Maharaja Surajmal Brij University**, Bharatpur (June 27, 2024 for one year)
18. Member, Board of Studies (BOS) of M.Sc.-Physics, **Madan Mohan Malviya University of Technology**, Gorakhpur (July 01, 2020 till date)
19. Member, Board of Studies (BOS) of the Department of Physics, **Mizoram University**, Aizawl (March 09, 2022 till date)
20. Member, Board of Studies (BOS) of B.Sc.-Electronics, **Siddarth University**, Kapilvastu, Siddarth Nagar (February 02, 2016 to February 01, 2019)
21. Member, Board of Studies (BOS) of M.Sc.-Physics & M.Sc.-Electronics, Dr. RamManohar Lohia Avadh University, Ayodhya (July 2000 to June 2002, July 2009 to June 2011, July 20, 2019 to March 31, 2023).
22. Member, Faculty Board (Science Faculty), Dr. RamManohar Lohia Avadh University, Ayodhya (July 19, 2014 to March 31, 2023)
23. Convener, Departmental Committee, Department of Physics & Electronics Dr. RamManohar Lohia Avadh University, Ayodhya (July 19, 2014 to July 19, 2017).

ACADEMIC DISTINCTION (17)

1. Conferred upon the **Certificate of Appreciation** by the employer institution 'Dr. RamManohar Lohia Avadh University' in **2021** for outstanding contribution in the

implementation of **NEP-2020**.

2. **Distinguished Researcher in Electronics Award (2020)** by Venus International Foundation, Chennai, Tamil Nadu
3. **IMRF Distinguished Scientist Award in Physics (2020)** by International Multidisciplinary Research Foundation, Vijayawada, Andhra Pradesh
4. **Best Outstanding Academician Award (2020)** by Kamarajar Institute of Education and Research (KIER), Theni, Tamil Nadu
5. Conferred upon the **Certificate of Appreciation** by the employer institution 'Dr. RamManohar Lohia Avadh University' in **2020** for the **Academic Achievements**.
6. **Best Scientist National Award (2018)** by IRDP Group of Journals, Chennai
7. Conferred upon the **Certificate of Appreciation** by the employer institution 'Dr. RamManohar Lohia Avadh University' in **2018** for the **Academic Achievements**.
8. **Maatee Ratan Samman 2017 (माटी रतन सम्मान)** by Ashfaq Ullah Khan Memorial Shaheed Shoadh Sansthan, Ayodhya (A State level Award)
9. Adarsh Vidya Saraswati Rastriya Puraskar 2017 (**आदर्श विद्या सरस्वती राष्ट्रीय पुरस्कार**) by Glacier Journal Research Foundation, Ahmedabad (A National level Award)
10. Sahitya Gaurav Samman 2017 (**साहित्य गौरव सम्मान**) by Hindi Prachar Prasar Sewa Sansthan, Ayodhya (Regional level Award)
11. Vidya Gaurav Samman 2019 by Kaundilya Sahitya Seva Samiti, Kadipur, Sultanpur (Regional level Award).
12. Certificate of Excellence in Research by Education ExpoTv.
13. HT Media Certificate of Achievement 2012 by Hindustan Times for Creative News Reporting (National level).
14. Certificate of appreciation by Organizing Committee of the National Seminar CCISC-2011.
15. One Research papers has been mentioned in May 30, 2012 issue of Electronic Business Journal (USA's research news bulletin).
16. **Highest Marks in M.Sc.** with Distinction in Two papers.
17. **Highest Marks & Distinction in Physics in B.Sc.**

ACADEMIC QUALIFICATION

High School (UP Board, Allahabad)	1981	Science Group	First	71.7%
Intermediate (UP Board, Allahabad)	1983	Science Group (P C M)	First	61.2%
B.Sc. (Avadh University, Faizabad)	1986	Mathematics, Physics, Chemistry	First	64.0%
M.Sc. (Avadh University, Faizabad)	1988	Physics (Electronics)	First	73.7%
PG Diploma (UP Gramya Vikas Vibhag)	1990	Computer Application	A	81.0%
Ph.D. (Avadh University, Faizabad)	1992	Physics	Research Field: Electroluminescence	
D.Sc. (Avadh University, Faizabad)	-	Physics	Thesis submitted on 05-01-2024	

TEACHING EXPERIENCE (30 Years +)

1. 30+ Years teaching experience at PG level (M.Sc-Physics & M.Sc.-Electronics students) with following designation/responsibilities.
 - (a) Professor (AGP 10000) 01-01-2009 till Date
 - (b) Associate Professor (AGP 9000) 01-01-2006 to 01-01-2009
 - (c) Reader (3700-5700) 25-09-2001 to 01-01-2006
 - (d) Senior Lecturer (3000-5000) 25-09-1997 to 25-09-2001
 - (e) Lecturer (2200-4000) 25-09-1992 to 25-09-1997

PRESENCE IN EDITORIAL BOARD / PEER REVIEWER (10)

1. Member, Editorial-Board, IJAREEIE (*International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering*), A Monthly peer reviewed journal, ISSN-online 2278-8875, ISSN-print 2320-3765
2. Member, Editorial-Board, JUSPS (*Journal of Ultra Scientist of Physical Sciences*), International peer reviewed journal, ISSN-online 2319-8052, ISSN-print 2231-3478
3. Member, Editorial-Board, IRJ (*International Researcher's Journal*), A Quarterly referred Journal, ISSN 2321-6301
4. Member, Editorial-Board, APM (*Applied Physics and Mathematics*), A Monthly peer reviewed International Journal, ISSN-online 2333-4886, ISSN-print 2333-4878)
5. Member, Reviewer-Board, IRJECE (*International Journal of Electronics & Communication Engineering*), ISSN- 2395-0587
6. Peer reviewer, IJPEDS (*International Journal of Power Electronics and Drive Systems*), p-ISSN: 2088-8694, e-ISSN 2722-256X
7. Peer reviewer, IJST (*Indian Journal of Science and Technology*), p-ISSN 0974-6846, e-ISSN 0974-5645
8. Peer reviewer, *Informacije MIDEM - Journal of Microelectronics, Electronic Components and Materials*, p-ISSN 0352-9045, e-ISSN 2232-6979
9. Peer reviewer, AJEEE, *American Journal of Electrical and Electronic Engineering*, p-ISSN 2328-7365, e-ISSN 2328-7357
10. Peer reviewer, JIAPS (*Journal of International Academy of Physical Sciences*), ISSN 0974-9373

INVITED LECTURES IN CONFERENCES / WORKSHOPS / WEBINARS / FDPs (37)

1. Invited Lecture on "NPN Sziklai Pair Small-Signal Amplifier for High Gain Low Noise Submicron Voltage Recorder", (December 23, 2022), 28th International Conference of International Academy of Physical Sciences (CONIAPS XXVIII), Vijayanagara Sri Krishnadevaraya University, Ballari, Karnataka, December 21-23, 2022
2. Faculty Induction Program (Orientation Program), UGC Human Resource Development Center, D.D.U. Gorakhpur University, Gorakhpur, January 06 – February 05, 2022 (Lecture Topic - Importance of ICT in reference to NEP-2020), dated January 07, 2022)

3. Invited talk on *“Issues and Challenges in the Implementation of National Education Policy 2020”*, Workshop at Siddarth University, Siddarthnagar, December 23, 2021
4. Invited talk on *“Implementation of National Education Policy 2020”*, NEP Workshop at Jhunjhunwala Group of Institutions, Ayodhya, December 13, 2021
5. Faculty Induction Program (Orientation Program), UGC Human Resource Development Center, D.D.U. Gorakhpur University, Gorakhpur, November 25 – December 24, 2021 (Lecture Topic - Role of ICT in NEP-2020: A Special Reference to E-content), dated November 27, 2021)
6. Faculty Induction Program, UGC Human Resource Development Center, D.D.U. Gorakhpur University, Gorakhpur, February 02 – March 03, 2021 (Lecture Topic - Success in Every Walk of Life Requires Positive Approach, dated February 18, 2021)
7. Invited Lecture on *“Low Frequency Small-Signal Amplifiers using BJT-JFET in Sziklai Pair Topology”*, (December 18, 2020), 26th International Conference of International Academy of Physical Sciences (CONIAPS XXVI), Manipal University, Jaipur, Rajasthan, December 18-20, 2020
8. Faculty Development Program on ‘Institutional Enrichment vis-à-vis skill development’, Department of Applied Sciences & Humanities, Rajkiya Engineering College, Mainpuri, U.P., November 24 - 28, 2020 (Lecture Topic - Positive Thinking, Behaviour and Attitude)
9. National Webinar on ‘Ways Ahead for Realizing Multidisciplinary Approach, Skill Development, Reskilling Teachers and New Courses in Higher Education according to New Education Policy 2020’, Netaji Subhash Chandra Bose Government Girls PG College, Lucknow, August 25, 2020 (Lecture Topic - Multidisciplinary Approach in Higher Education as per New Education Policy 2020)
10. National Webinar on ‘National Education Policy: Prospects and Challenges’, D.D.U. Gorakhpur University, September 9, 2020 (Lecture Topic - Issues and Challenges in NEP)
11. National E-Seminar on Ideathon ‘Remediation of Migrant Workers and Society using Technology, Institute of Engineering and Technology, Dr. Ram Manohar Lohia Avadh University, Ayodhya’, 10-15 June 2020 (Lecture Topic - Skill based Education)
12. National E-Seminar on ‘Live Classroom & Course Creation through Kinmbus Digital Library, Institute of Engineering and Technology, Dr. Ram Manohar Lohia Avadh University, Ayodhya, May 21, 2020 (Lecture Topic - Online Education)
13. International Webinar on ‘Next Generation Computing Systems’, Institute of Engineering and Technology, Dr. Ram Manohar Lohia Avadh University, Ayodhya, May, 17, 2020 (Lecture Topic - Next Generation Computing Systems)
14. National Seminar/Webinar on ‘Significant Role of E-Education through Social Distancing During Lockdown of Covid-19 Pandemic’, Institute of Engineering and Technology, Dr. Ram Manohar Lohia Avadh University, Ayodhya, April, 8-9, 2020 (Lecture Topic - Role of E-Education During Lockdown in Covid-19 Pandemic)
15. National Webinar on ‘Virtual Lab’, Institute of Engineering and Technology, Dr. Ram Manohar Lohia Avadh University, Ayodhya, April, 16, 2020 (Lecture Topic - Virtual Lab)
16. National Webinar on ‘Unlocking the Lockdown Through E-Examination’, Institute of Engineering and Technology, Dr. Ram Manohar Lohia Avadh University, Ayodhya, April, 20, 2020 (Lecture Topic - Unlocking the Lockdown Through E-Examination)

17. Invited Lecture on *"Fundamentals of PSpice"*, (January 28, 2020), National Workshop on Modelling and Simulation on Microstrip Antenna, Circuits and Materials (MASMACM-2020), Dr. Ram Manohar Lohia Avadh University, Ayodhya, January 27- February 02, 2020
18. Invited Lecture on *"Small-Signal Amplifying Systems for Amplification of Low Frequency Beta and Seismic Waves"*, (December 31, 2019), 25th International Conference of International Academy of Physical Sciences (CONIAPS XXV), Guru Jambheshwar University of Science and Technology, Hisar, Haryana, December 29-31, 2019
19. Invited Lecture on *"Model of a Small Signal Amplifying System for Amplification of β and Seismic Waves"*, 62nd Annual IETE Convention & International Conference on Industry 4.0, IETE and Institute of Engineering & Technology, Dr. RamManohar Lohia Avadh University, Ayodhya, U.P., September 28 - 29, 2019
20. Invited Lecture on *"Mobile Phone Embedded System"*, One Week National Workshop on Embedded System Design, Institute of Engineering & Technology, Dr. Ram Manohar Lohia Avadh University, Ayodhya, U.P., May 28 - June 02, 2019
21. Invited Lecture on *"Success in Every Walk of Life Requires Positive Approach"*, (May 21, 2019), National Workshop on Research Methodology, Dr. RamManohar Lohia Avadh University, Ayodhya, U.P., May 20-26, 2019
22. Invited Lecture on *"Small-Signal Amplifying System for Amplification of β and Seismic Waves"*, (May 11, 2019), 4th International Conference on Microelectronics, Computing & Communication Systems (MCCS-2019), ARTTC, BSNL, Hazaribag Road, Ranchi, Jharkhand, May 11-12, 2019
23. Invited Lecture on *"Significance of IoT"*, One Week National Workshop on Embedded System Design, Institute of Engineering & Technology, Dr. Ram Manohar Lohia Avadh University, Ayodhya, U.P., April 01-06, 2019
24. Invited Lecture on *"Hazards of Mobile Network"*, One Week National Workshop on Embedded System Design, Institute of Engineering & Technology, Dr. Ram Manohar Lohia Avadh University, Ayodhya, U.P., March 10-14, 2019
25. Invited Lecture on *"Role of ICT in Higher Education Teaching and Learning Practices"*, (February 24, 2019), National Conference on Innovative Pedagogical Approaches: Paradigm Shift in Teaching and Learning, Ranvir Rananjay Post Graduate College, Amethi, U.P., February 23-24, 2019
26. Invited Lecture on *"Small-Signal Amplifier Using BJT-JFET in Sziklai Pair Topology"*, (February 28, 2019), National Conference on Recent Advances in Material Science and Electronics (RAMSE-2019), Department of Physics and Electronics, Dr. Ram Manohar Lohia Avadh University, Ayodhya, U.P., February 27-28, 2019
27. Invited Lecture on *"Transcriptor: A Leading Element for Alternative Computers"*, (November 12, 2018), National Conference on Role of Modern Biological Sciences for Enhancing Farmer's Economy & Welfare of the Society, Ranvir Rananjay Post Graduate College, Amethi, U.P., November 12-13, 2018
28. Invited Lecture on *"Small-Signal Amplifying Systems Using Sziklai Pair Topology"*, (November 17, 2018), 23rd International Conference of International Academy of Physical Sciences (CONIAPS XXIII), Kathmandu, NEPAL, November 16-18, 2018

29. Invited Lecture on *"A Small-Signal Amplifying System for Amplification of Seismic Waves"*, (October 3, 2018), National Conference on Environmental Health, Climate Change and Its Management (NCECM-2018), Department of Environmental Sciences, Dr. RamManohar Lohia Avadh University, Ayodhya, U.P., October 3-4, 2018
30. Invited Lecture on *"Development of Small-Signal Amplifying System Useful to Amplify Seismic Waves"*, (May 21, 2018), Conference on Science and Technology (CST2018), Mahendranagar, Kanchanpur, NEPAL, May 21-22, 2018
31. Invited Lecture on *"Small Signal Amplifying System with Sziklai pair Topology"* (April 15, 2018), 22nd International Conference of International Academy of Physical Sciences on 'Emerging Trends in Physical Sciences' (CONIAPS XXII), Dr. RamManohar Lohia Avadh University, Ayodhya, April 13-15, 2018
32. Invited Lecture on *"Design and Analysis of Small Signal Amplifiers using Sziklai pairs as Active Components"* (July 16, 2017), 20th International Conference of International Academy of Physical Sciences on 'Recent Advances in Physical Sciences and Future Challenges' (CONIAPS XX), Osmania University, Hyderabad, July 14-16, 2017
33. Invited Lecture on *"Sziklai pairs in Small-signal Amplifiers"* (March 26, 2017), Short Term Course on 'Emerging Trends in Material Science' (STCETMS-2017), Madan Mohan Malaviya University of Technology, Gorakhpur, March 23-29, 2017
34. Invited Lecture on *"Foundation of Physics"*, J.B. Academy (A prestigious CBSC School), Ayodhya August 19, 2017
35. Invited Lecture on *"Optical Computers"*, Physics Department, M.L.K. P.G. College, Balrampur October 07, 2016
36. Invited Lecture on *"Sziklai pairs in Small-signal Amplifiers"* (Sept. 26, 2014), National Conference on Integrated Circuits and Signal Processing (ICSP-2014), Amity University, Lucknow Campus, Lucknow, September 25-26, 2014
37. Invited Lecture on *"Successful Research Approach Requires Positive Attitude"* in 'UGC teacher's training program' of Academic Staff College, D.D.U. Gorakhpur University, Gorakhpur, U.P. (September 28, 2013)

CONFERENCE / SEMINAR ASSIGNMENTS

1. Reviewer and Member Program Technical Committee, SPIN-2023 (International Conference on Signal Processing & Integrated Networks), IEEE Indexed Conference.
2. Reviewer and Member Program Technical Committee, SPIN-2019 (International Conference on Signal Processing & Integrated Networks), IEEE Indexed Conference.
3. Session Chair (February 28) National Conference on Recent Advances in Material Science and Electronics (RAMSE-2019), Dr. RamManohar Lohia Avadh University, Ayodhya, U.P., February 27-28, 2019
4. Convener, 22nd International Conference of International Academy of Physical Sciences (CONIAPS XXII), 13-15 April 2018 at Dr. RamManohar Lohia Avadh University, Ayodhya, U.P., India

5. Conference Co-Chair in First International Conference on Computational and Mathematical Methods in Engineering & Technology (iCOMET'17), December 18-20, 2017, Melbourne, Australia
6. Session Chair (Sept.26) in National Conference on Integrated Circuits and Signal Processing (ICSP-2014), Amity University, Lucknow Campus, Lucknow, September 25-26, 2014
7. Session Chair (March 22) in XVI Annual Conference of International Academy of Physical Sciences (CONIAPS 2014), PDPM IIIT, Jabalpur, M.P., March 20-22, 2014
8. Organizing Secretary, National Seminar on Present Scenario of Higher Education in India: Prospects & Challenges, Dr. RamManohar Lohia Avadh University, Ayodhya, September 19-20, 2015
9. Organizing Secretary, National Seminar on Emerging Trends in Electronics & Computer, March 11-12 1999 at Department of Physics & Electronics, Dr. R.M.L. Avadh University, Ayodhya
10. Member, Conference-Technical-Committee (MCCS-2015), 1st International Conference on Micro-electronics, Computing & Communication Systems, at ARTTC BSNL, Ranchi, November 14-15, 2015
11. Member, Conference-Technical-Committee (NCCS-2015), 1st International Conference on Nano-electronics, Circuits & Communication Systems, at ARTTC, BSNL, Ranchi, April 25-26, 2015
12. Member, National Advisory Committee (ICSP-2014), National Conference on Integrated Circuits and Signal Processing, Amity University, Lucknow Campus, Lucknow, September 25-26, 2014
13. Member, Conference-Technical-Committee, (ICCS-2014), International Conference on Circuits, System and Simulation, August 17-18, 2014, Nottingham, UK
14. Member, Organizing Committee, National Seminar on Materials and Its Applications; February 27-28, 2003, Dr. R.M.L. Avadh University, Ayodhya
15. Member, Organizing Committee, National Seminar on Higher Education in India: Vision and Action; February 7, 2000 at Dr. R.M.L. Avadh University, Ayodhya
16. Coordinator, Workshop on 'Computer Operation and Software Applications', Dr. RamManohar Lohia Avadh University, Ayodhya, 9-15 March 1995

SOCIAL AFFILIATION

1. Member, Rotary Club (An International Social Service Platform), Ayodhya
2. Honorary Journalist, Hindustan Times, Lucknow (May 2009 to February 2019)
3. Former Member, Management Board (2010-2019), Maharaja Public School, Ayodhya, (an Intermediate level CBSE based public school)
4. Former Member, School Advisory Board (2006-2015), Navodaya Vidyalaya, Ayodhya (an Intermediate level CBSE based school of Government of India)
5. Member, School Management Committee (April 1, 2016 - March 31, 2019), Jingle Bell Academy, Ayodhya (an Intermediate level CBSE based leading public school of Ayodhya)
6. Vice President, District Badminton Association, Ayodhya

CONSULTANCY SERVICES

1. Completed Consultancy Project (review and authentication of competitive questions) worth Rs. 46,240/- for Tata Consultancy Services (TCS) during April 23 to May 07, 2022.
2. Completed Consultancy Project (review and authentication of competitive questions) worth Rs. 34,340/- for Tata Consultancy Services (TCS) during May 25 to June 03, 2022.

CURRENT RESEARCH PROJECTS

1. Development and Analysis of Small-Signal Sziklai pair Amplifiers and other Linear Electronic Circuits, Supported by Department of Higher Education, UP (April 20,2022) **(Principal Investigator)**
2. Artificial Intelligence Based EEG Monitoring system for Neonatal Intensive Care Unit (NICU) of Japanese Encephalitis, Supported by Department of Higher Education, UP (August 10,2021) **(Co-Principal Investigator)**

RESEARCH SUPERVISION

Supervised 19 Ph.D. Scholars (As Supervisor - 18, As Co-supervisor - 01). (Research Scholar / Field/ Topic / Degree Awarding Date)

1. Dinesh Singh (Physics): Development of Red Light Emitting Electroluminophors and Their Characteristics (25-01-2002)
2. Raghavendra Pratap Singh (Physics): Computation of Solar Constants for Prediction of Solar Radiations (12-12-2003)
3. Prem Kumar Singh (Physics): Development of Binary Electroluminophors of II-VI Group Compounds and Their characteristics (01-08-2009)
4. Kranti Kirnesh Dubey (Electronics): Development, Study and Simulation of Electronic Circuits, Designed by Coupling of Active Electronic Components (17-01-2009)
5. Satyndra Nath Tiwari (Physics): Development of Linear Electronic Circuits by Coupling of Various Junction Devices and Their Analysis Through Simulation Software (28-02-2009)
6. Geetika Srivastava (Electronics): Development and Modification of Wave-shaping Circuits through PSpice and their Physical Verification (16-09-2009)
7. Arvind Kumar Dewedi (Physics): Development and Simulation Studies of Electronic Circuits, Configured by Coupling of Diodes and Transistors (16-05-2010)
8. Jitendra Singh (Physics): Development, Modification and Analysis of Diode and BJT Based Linear Circuits Through PSpice (24-12-2013)
9. Beena Pandey (Physics): Development and Analysis of Modified Small Signal Amplifiers Through PSpice (28-06-2014)
10. Meena Singh (Electronics): Floating Admittance Technique for Programming Functions of Amplifiers (01-08-2014)
11. Susmrita Srivastava (Electronics): Development and Analysis of Modified JFET and MOSFET Amplifiers Through PSpice (23-09-2014)
12. Sashwati Manohar (Physics): Electro-optical and Electrical Properties of Mesogenic Materials and their Polymer Composites (07-02-2015)

13. Ramendra Singh (Electronics): Development, Modification and Analysis of Small Signal BJT Amplifiers Through PSpice (16-08-2016)
14. Shipra Saraswat (Electronics): Development of Real Time System for Efficient Processing of ECG Data Using Techniques of Artificial Intelligence (01-11-2017)
15. Pratima Soni (Physics): Development and Analysis of Darlington and Sziklai Pair Based Linear Electronic Circuits (16-09-2024)
16. Syed Shamroz Arshad (Physics): Design and Analysis of High Gain Low Power Small Signal Sziklai Pair Amplifiers (Registered on January 15, 2021)
17. Shefali Jaiswal (Electronics): OTA Based Tunable Low Pass Filter Design for Biomedical Signal Processing Unit (Registered on November 12, 2021)
18. Noopur Kesarwani (Electronics): Research Topic is yet to be decided (Allotted on November 12, 2021)
19. Sachchida Nand Singh (Physics): Low Power Low Voltage Gm-C OTA Design for Biomedical Application (Registered on March 03, 2022)

EXAMINATION RELATED RESPONSIBILITIES

1. **State Coordinator**, 'Combined Pre-Medical Test (**CPMT-2016**) (Conducting body - Dr. RamManohar Lohia Avadh University, Ayodhya, U.P.)
2. **State Deputy Convener**, 'Common Eligibility Test Ph.D.-2012 (**CET-2012**)' (Conducting body - Dr. RamManohar Lohia Avadh University, Ayodhya, U.P.)
3. District Coordinator, State level B.Ed. Entrance Examination 2007 (organized by C S J M Kanpur University, U.P.)
4. **Coordinator**, 'Common Eligibility Test for Ph.D.-2016 (**PhD Entrance Test-2016**)' of Dr. RamManohar Lohia Avadh University, Ayodhya, U.P.
5. Deputy District Coordinator, State level B.Ed. Entrance Examinations 2008, 2009 and 2010
6. Coordinator, M.Ed. Entrance Examination-2011 of Dr. RamManohar Lohia Avadh University, Ayodhya, U.P.
7. Deputy Coordinator, B.Ed. Entrance Examination 2005 and 2006 of the University.
8. Deputy Coordinator, M.Ed. Entrance Examination 2006 and 2007 of the University
9. Center Superintendent, University (Campus) Examinations in 2001-02, 2005-06, 2006-07 and 2007-08 sessions
10. Center Superintendent (Campus Center), B.Ed., M.Ed., B.PEd. and Law Entrance Examination of the University in 2004-05 and 2006-07
11. Center Superintendent, CPMT-2001, 2003, 2006 and 2007 at Dr. R.M.L.Avadh University, Ayodhya Center.
12. Center Observer, M.Ed.-Entrance-2014, B.Ed.-Entrance-2011, CPMT-2015, B.Ed.-Entrance-2017 at different Examination Centers of Ayodhya

CO-CURRICULAR AND OTHER ACTIVITIES

1. Conducted (Anchored) 16th Convocation program of Narendra Dev University of Agriculture and Technology, Kumarganj, Ayodhya on special invitation of the varsity's Vice chancellor
2. Chief Editor of SOUVENIRS / University Annual Magazines published on the occasion of 13th 14th 15th 18th 19th and 22nd Convocations of Dr. R.M.L. Avadh University, Ayodhya
3. Approved Counselor of IGNOU Indira Gandhi National Open University (Counselor Code-093007) for CIC course (Since 2001)
4. Convener, North Zone Inter-varsity Kho-Kho (M/F) and North Zone Intervarsity Cricket (F) tournaments during 2011-12
5. Convener, District level 'Science Awareness Year-2004' programs
6. Co-convener, Inter Collegiate Youth Festivals (16,17 January 1999; 2,3 November 1999; 21,22 December 2001 and 21,22 November 2002)
7. Member, Selection Committees (more than two dozen) of various Degree Colleges and Inter Colleges.
8. Presented more than a dozen of Radio-talks at Akashwani, Ayodhya
9. Delivered special lecture in 'Teacher's training program' of Navodaya Vidyalaya Samiti, Navodaya Vidyalaya, Ayodhya, U.P. (July 21, 2014)

REFRESHER COURSE / TRAINING PROGRAM

1. Six Days Training Program on Computer Interfaced Science Experiments, Inter-University Accelerator Center (IUAC), New Delhi, April 29 to May 4, 2013
2. Refresher Course in Physics, July 24 to August 13, 2001 (21 days), University of Lucknow, Lucknow.
3. Refresher Course on "Experimental Physics"; April 10 to May 9, 2001 (30 days), Aligarh Muslim University, Aligarh
4. One-day Training Program on 'NICNET Communication Systems', NIC, Lucknow, 30-11-1997
5. Refresher Course on "Recent Advances in General and Material Science, Microelectronics and Computer Applications"; August, 12 to September, 2, 1996 (21 days), A.P.S. University, Rewa (M.P.)

PRESENTATION IN INTERNATIONAL/NATIONAL CONFERENCES/WORKSHOPS (30)

1. International Conference on Recent Advances in Mathematical and Physical Sciences (RAMPS-2022), December 17-18, 2022, Dr. Ram Manohar Lohia Avadh University, Ayodhya (Presented Paper – New Circuit Model of Small-signal Amplifiers with Darlington pairs under Sziklai Topology)
2. National Conference on Smart Materials and Devices for Sustainable Technologies, L.B.S. Degree College, Gonda, UP, June 20-21, 2022, (Presented Paper - Low Frequency Small Signal Amplifier using BJT-JFET in Sziklai Pair Topology)

3. 27th International Conference of International Academy of Physical Sciences (CONIAPS XXVII) on Recent Advances in Applied Physics, October 26-28, 2021 at North Eastern Regional Institute of Science and Technology (NERIST), Itanagar, Arunachal Pradesh (Presented Paper - Small-signal Amplifiers with Sziklai Pair Topology: Case with BJT-JFET Hybrid Unit)
4. 3rd International Conference on Smart Innovations in Communication and Computational Sciences (ICSICCS-2020), February 27-28, 2020, Institute of Engineering and Technology, Dr. Ram Manohar Lohia Avadh University, Ayodhya (Presented Paper – Review of Ultra Low Power CMOS Amplifier for Bio-electronic Sensor Interface)
5. National Conference on Soft Matter, Department of Physics (NCSM), Deen Dayal Upadhyaya Gorakhpur University, Gorakhpur, March 27-28, 2018 (Presented Paper- Small-Signal Amplifier with Modified Sziklai Pair Topology)
6. National Conference on Functional Materials, Department of Physics, Deen Dayal Upadhyaya Gorakhpur University, Gorakhpur, March 10-12, 2016 (Presented Paper- Small-Signal Amplifying System with Three Dissimilar Active Devices)
7. IEEE International Conference on Advances in Electrical, Electronics & System Engineering (IEEE-ICAESE 2016), November 14-16, 2016, Kuala Lumpur, MALAYSIA (Presented Paper – New Small-signal Amplifying System with Sziklai pairs in Triple-Transistor Topology)
8. National Seminar on Human Rights and Social Justice, K.S. Saket P.G. College, Ayodhya, Faizabad, February 28-29, 2016 (Presented Paper- Impact of ICT on Social Sector)
9. 18th International Conference of International Academy of Physical Sciences (CONIAPS XVIII) on Recent Trends in Physical Sciences, December 22-24, 2015 at University of Allahabad, Allahabad, U.P., India (Presented Paper - Small-signal Amplifiers with PNP and NPN Sziklai Pairs)
10. IEEE Radio and Antenna Days of the Indian Ocean (IEEE RADIO 2015), September 21-24, 2015, Belle Mare, MAURITIUS (Presented Paper - Novel Circuit of Small-signal Amplifier with BJT-MOSFET Hybrid Unit in Sziklai Pair Topology)
11. National Seminar on Present Scenario of Higher Education in India: Prospects & Challenges, Dr. RamManohar Lohia Avadh University, Ayodhya, September 19-20, 2015 (Presented Paper- Importance and Necessity of ICT in Higher Education)
12. 17th International Conference of International Academy of Physical Sciences (CONIAPS XVII) on Emerging Trends in Physical Sciences & Technology, January 16-18, 2015 at University of Rajasthan, Jaipur (Presented Paper - Small-signal FET Amplifiers with Triple Darlington Topology)
13. 11th IEEE International Conference on Semiconductor Electronics (IEEE-ICSE 2014), August 27-29, 2014, Kuala Lumpur, MALAYSIA (Presented Paper - Two-stage Small-signal Amplifier with Darlington and Sziklai pairs)
14. XVI International Conference of International Academy of Physical Sciences (CONIAPS 2014), March 20-22, 2014 at PDPM IIIT, Jabalpur, M.P. (Presented Paper - Small-signal Amplifiers with Hybrid Combination of Three Unlike Active Devices in Triple Darlington Topology)

15. International Training Workshop on FPGA Design for Scientific Instrumentation and Computing' November 11 to 22, 2013 at International Center for Theoretical Physics (ICTP), Trieste, ITALY.
16. 2nd International Conference on Engineering Mathematics and Physics (ICEMP 2013), June 15-16, 2013, Colombo, SRI LANKA (Presented Paper - A New Circuit Model of Small-signal Sziklai pair amplifier)
17. 4th International Conference on Computer Modeling and Simulation (ICCMS 2013), February 24-25, 2013, Rome, ITALY (Presented Paper - Development and Qualitative Analysis of a New Circuit Model of Two-stage Small-Signal Sziklai pair Amplifier)
18. 15th International Conference of International Academy of Physical Sciences (CONIAPS XV), December 9-13, 2012, Thanyaburi, THAILAND (Presented Papers - A Small-signal Amplifier Developed by using Three Dissimilar Active Devices in Triple Darlington Topology and New Circuit Model of Small-signal Amplifier Developed by using MOSFETs in Triple Darlington Configuration)
19. 10th IEEE International Conference on Semiconductor Electronics (IEEE-ICSE 2012), September 19-21, 2012, Kuala Lumpur, MALAYSIA (Presented Papers - Qualitative Study of a New Circuit Model of Small-signal Amplifier using Sziklai pair in Compound Configuration and New Circuit Models of Complementary-Symmetry Class-AB and Class-B Push-Pull Amplifiers)
20. National Seminar on Social Change in Modern India, K.S. Saket P.G. College, Ayodhya, Faizabad, February 20-21, 2011 (Presented Paper- Electronics for Social Change in Modern India)
21. National Seminar on Current Trends in Mathematics with Special Focus on Operation Research and Computers, Department of Mathematics and Statistics, Dr. R.M.L. Avadh University, Ayodhya, March 28-29, 2010 (Presented Paper- Qualitative Analysis of Small Signal Wide Band Triple Darlington Amplifier)
22. National Symposium on Advances in Material Science, Department of Physics, D.D.U. University, Gorakhpur, March 17-19, 2005 (Presented Paper- On the Magneto-optical Effect of Modulated Diode Lasers)
23. National Seminar on Materials and Its Applications; February 27-28, 2003, Dr. R.M.L. Avadh University, Ayodhya (Presented Papers- Comparison of Electroluminescence in ZnO-CdO and ZnO-CdS Base Binary Systems and Spectral Distribution and Enhancement Studies of ZnO Base Electroluminophors)
24. National Seminar on Higher Education in India: Vision and Action; February 7, 2000 at Dr. R.M.L. Avadh University, Ayodhya
25. National Seminar on Emerging Trends in Electronics & Computer; March 11,12 1999, Dr. R.M.L. Avadh University, Ayodhya (Presented Papers- Photoluminescence in Double Band Emitting (ZnS+HgO):Cu,Mn(H) Binary Systems and Electroluminescent Behaviour of Red Light Emitting ZnSe:Mn Phosphors)
26. Second Conference of the 'International Academy of Physical Sciences', Guru Ghasidas University, Bilaspur, 13-14 December 1997 (Presented Paper- Electroluminescent Studies of ZnO Base Electroluminophors and Electroluminescence in ZnO-CdO and ZnO-CdS Base Binary System)

27. Workshop on 'Computer Operation and Software Applications', Dr. RamManohar Lohia Avadh University, Ayodhya, 9-15 March 1995 (As Coordinator) (Presented Talk- Computer and Its internal Organization)
28. International Workshop on 'The Use of Computers in Teaching Physics', R.D. University, Jabalpur, M.P., December 3 to 9, 1992
29. National Seminar on CAD/CAM, KNIT and HAL, Sultanpur, 27-29 January 1993
30. Diamond Jubilee Session of 'The National Academy of Sciences', Allahabad University, 3-5 May 1991 (Presented Papers- 1. Electroluminescent Brightness Wave Forms in Triple Band Electroluminophors ZnS:Cu,Mn(H) and 2. Photoluminescence of Doubly Activated Zinc Sulphide Phosphors)

TOTAL PUBLICATIONS (148)

Books-08, Research papers-122, General Articles in News Paper-18)

PUBLISHED BOOKS (08)

1. *Small Signal Amplifiers with JFETs and MOSFETs in Darlington's Topology*, LAMBERT Academic Publishing, GERMANY, ISBN-978-36-5962-783-5 (2014)
2. *Introduction to Modern Instrumentation: Methods and Techniques*, ARCLER Press, CANADA, ISBN 978-1-77407-370-4 (2019)
3. *Introduction to Electrical Measurements*, ARCLER Press, CANADA, ISBN 978-1-77407-323-0 (2019)
4. *Digital Communication*, Shree Publishers & Distributers, ISBN-978-81-8329-807-0 (2017)
5. *Basic Electronics Engineering*, MGS Publications (P.) Ltd., ISBN-978-93-8902-202-5
6. *Organic Semiconductors for Optoelectronics*, ARCLER Press, CANADA, ISBN 978-1-77469-530-2 (2022)
7. *The Study of Elementary Particles*, ARCLER Press, CANADA, ISBN 978-1-77469-430-5 (2022)
8. *Samajik Samrasta*, Dr. RamManohar Lohia Avadh University, ISBN 978-93-88003-01-8 (2018)

PUBLISHED RESEARCH PAPERS (121)

(A) PAPERS PUBLISHED IN JOURNALS – 95

1. *Issues and Challenges in Small-Signal Low-Power Amplifiers: A Review*, **S.N. Shukla**, S.S. Arshad, K. Thakur, G. Srivastava, Indian Journal of Science and Technology, ISSN 0974-6846 (print) ISSN 0974-5645 (electronic), 17(36), pp 3787-3799, 26 September **2024**, <https://doi.org/10.17485/IJST/v17i36.2171> (**Web of Science/ UGC Care List**)
2. *Design of Ultra-wideband Sziklai pair based LNAs for Wireless Communication Applications*, S. Shamroz Arshad, G. Srivastava, **S.N. Shukla**, International Journal of Information Technology, ISSN: 2511-2104 (print), ISSN: 2511-2112 (electronic), 15 September **2024**, <https://doi.org/10.1007/s41870-024-02188-z> (**Scopus Indexed**)
3. *Studies on Temperature Dependent Volume of Nanomaterials with Varying Shape and Size using Suitable Equation of State*, R. Pathak, B.K. Pandey, R. L. Jaiswal, M.K. Upadhyay,

- S.N. Shukla**, Chemical Physics Letters, 852, ISSN 0009-2614, 141512, August **2024**, DOI: <https://doi.org/10.1016/j.cplett.2024.141512> (**Scopus Indexed**)
4. *A Low-Voltage Tw-Stage CMOS OTA with Enhanced DC-Gain for Biomedical Applications*, S.N. Singh, G. Srivastava, S. S. Arshad, **S.N. Shukla**, Journal of Rajasthan Academy of Physical Sciences, ISSN 0972-6306, Vol.23, No. 1&2, June **2024**, pp. 72-81 (**Web of Science/UGC Care List**), URL: <http://raops.org.in>
 5. *Low Power CMOS Gm-C Based Low Pass Filter for Front End Neural Signal Processing*, A. Dixit, G. Srivastava, A. Kumar, **S. N. Shukla**, International Journal of Power Electronics and Drive Systems, ISSN 2088-8694, Vol.15, No.1, pp.566-572, March **2024**, DOI: 10.11591/ijpeds.v15.i1.pp566-572 (**Scopus Indexed**)
 6. *Studies on Thermal Conductivity of Metallic Nanoparticles with Varying Shape and Size*, D. Sharma, B.K. Pandey, R. L. Jaiswal, J. Gupta, **S.N. Shukla**, Chemical Physics Letters, ISSN 0009-2614, 141363, May **2024**, <https://doi.org/10.1016/j.cplett.2024.141363>, (**Scopus Indexed**)
 7. *Formulation of an appropriate equation of state to predict the melting temperature of metallic solids*, J. Patel, J. Gupta, S. Mishra, **S.N. Shukla**, P. Singh, B.K. Pandey, Computational Condensed Matter, ISSN 2352-2143, Volume 38, March **2024**, e00888, DOI: <https://doi.org/10.1016/j.cocom.2024.e00888>, (**Scopus Indexed**)
 8. *Novel Complementary Sziklai Pair Based High Gain Low Noise Small-Signal Amplifiers*, **S.N. Shukla**, S. Shamroz Arshad, G. Srivastava, International Journal of Power Electronics and Drive Systems, Vol.14, No.4, December **2023**, pp. 2283-2292, ISSN 2088-8694, DOI: 10.11591/ijpeds.v14.i4.pp2283-2292 (**Scopus Indexed**)
 9. *Sziklai Pair based Small-signal Amplifier with BJT-MOSFET Hybrid Unit at 180nm Technology*, **S.N. Shukla**, S. S. Arshad, K. Thakur, G. Srivastava, Journal of Ravi Shankar University, Part-B, ISSN 0970-5910, Vol.36, No.2, December **2023**, pp. 41-59, DOI: 10.52228/JRUB.2023-36-2-4 (**Peer Reviewed**)
 10. *Design and Analysis of Wideband PNP Sziklai Common Collector Amplifier with High Current Gain*, S.N. Shukla, S.S. Arshad, S. Pratima, A.K. Sharma, G. Srivastava, Indian Journal of Science and Technology, P-ISSN 0974-6846 E-ISSN 0974-5645, Vol. 16, No.42, pp.3743-3755, November 12, **2023**, <https://doi.org/10.17485/IJST/v16i42.689>, (**Web of Science/ UGC Care List**)
 11. *High Gain Narrow Band Small Signal Hybrid Unit Darlington Pair Amplifier*, P. Soni, G. Srivastava, S.S. Arshad, A.K. Sharma, **S.N. Shukla**, Journal of Theoretical and Applied Information Technology, (E-ISSN 1817-3195 / ISSN 1992-8645), Vol. 101, No. 19, October 15, 2023, pp. 6102-6113, (**Scopus Indexed**), <https://www.jatit.org/volumes/Vol101No19/22Vol101No19.pdf>
 12. *Sziklai Pair and Darlington Pair RTL Inverters for High Drive Current Applications*, S. Shamroz Arshad, G. Srivastava, **S.N. Shukla**, Journal of Rajasthan Academy of Physical Sciences, ISSN 0972-6306, Vol.22, No. 1&2, June 2023, pp. 22-40 (**Web of Science/UGC Care List**), URL: <http://raops.org.in>
 13. *Unified model for the prediction of thermophysical properties of nanometals*, T. Kumari, B.K. Pandey, J. Gupta, R. L. Jaiswal, S.N. Shukla, Solid State Communications, ISSN 0038-1098, Volume 371, June 2023, 115254, pp. 01-13, (**Scopus Indexed**), <https://doi.org/10.1016/j.ssc.2023.115254>

14. *N-Channel MOSFET Small-Signal Darlington Pair based Low-Power Wide-Band Amplifier*, P. Soni, S. Shamroz Arshad, G. Srivastava, **S.N. Shukla**, Journal of International Academy of Physical Sciences, ISSN 0974-9373, Vol.27, No.1, March 2023, pp. 35-56 (**Peer Reviewed**), <http://www.iaps.org.in/journal/index.php/journaliaps/article/view/964>
15. *Darlington Pair based Small-signal Amplifier under Triple-Transistor Topology*, S. Shamroz Arshad, **S.N. Shukla**, A.K. Sharma, G. Srivastava, Journal of International Academy of Physical Sciences, ISSN 0974-9373, Vol.26, No.4, December 2022, pp. 427-442, (**Peer Reviewed**)
<http://www.iaps.org.in/journal/index.php/journaliaps/article/view/960/821>
16. *Small Signal Sziklai Pair based Tuned Amplifiers with Low Power High Gain*, **S.N. Shukla**, S. Shamroz Arshad, Pratima Soni, G. Srivastava, A. Dixit, A. Kumar, Journal of International Academy of Physical Sciences, ISSN 0974-9373, Vol.26, No.3, September 2022, pp. 281-306, (**Peer Reviewed**)
<http://www.iaps.org.in/journal/index.php/journaliaps/article/view/944/815>
17. *Operational Transconductance Amplifier Based Universal Active Filter for Biomedical Signal Processing Unit*, S. Shamroz Arshad, **S.N. Shukla**, J. Singh, International Journal of Scientific Research in Science and Technology, p-ISSN 2395-6011, e-ISSN 2395-602X, Vol.9, Issue.14, June 2022, pp. 86-93, (**Peer Reviewed**), DOI: 10.32628/IJSRST2291414
18. *NPN Sziklai Pair Small Signal Amplifier for High Gain Low Noise Submicron Voltage Recorder*, **S.N. Shukla**, S. Shamroz Arshad, G. Srivastava, International Journal of Power Electronics and Drive Systems, ISSN 2088-8694, e-ISSN 2722-256X, Vol.13, No.1, March 2022, pp. 11-22, DOI: 10.11591/ijpeds.v13.i1.pp11-22 (**Scopus Indexed**)
19. *New Circuit Model of Small Signal Amplifiers with Darlington pairs under Sziklai pair Topology*, **S.N. Shukla**, Journal of Rajasthan Academy of Physical Sciences, Vol.20, ISSN 0972-6306, No. 1&2, pp 119-138, June 2021, URL: <http://raops.org.in> (**Web of Science/UGC Care List**)
20. *Development of Low Frequency Small Signal Amplifier using BJT-JFET in Sziklai pair Topology*, **S.N. Shukla**, P. Soni, N.K. Chaudhary, G. Srivastava, International Journal of Recent Technology and Engineering, Vol.9, Issue 3, DOI:10.35940/ijrte.C4365.099320, ISSN 2277-3878, pp 217-223, 2020 (**Scopus Indexed**)
21. *Small Signal Amplifier with P-Type Sziklai Pair*, **S.N. Shukla**, S. Shamroz Arshad, P. Soni, G. Srivastava, Journal of International Academy of Physical Sciences, Vol. 24(3), ISSN 0974-9373, pp. 333-346, 2020 (**Peer Reviewed**)
URL: <http://www.iaps.org.in/journal/index.php/journaliaps/article/view/31/20>
22. *Development of a Small Signal Amplifier with Modeling of BJT-JFET Unit in Sziklai Pair Topology*, **S.N. Shukla**, G. Srivastava, P. Soni, R. Mishra, International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol.8, Issue 5, DOI:10.15662/IJAREEIE.2019.0804014, ISSN-2278-8875, pp.1530-1538, 2019 (IF-6.392)
23. *Qualitative Analysis of High Gain Small-signal Amplifier with MOSFET Current Mirror*, **S.N. Shukla**, International Journal of Applied Engineering Research, ISSN-0973-4562, Vol.14, Number 1, pp. 55-61, 2019 (IF-0.14) (UGC List Journal serial number 64529)
URL: https://www.ripublication.com/ijaer19/ijaerv14n1_08.pdf

24. *On Low Power CMOS Operational Transconductance Amplifier Structures*, G. Srivastava, Aditya K. Sharma, **S.N. Shukla**, A. Dixit, A. Kumar, Journal of International Academy of Physical Sciences, Vol. 23(3), ISSN 0974-9373, pp. 283-295, 2019, **(Peer Reviewed)**
URL: <http://www.iaps.org.in/journal/index.php/journaliaps/article/view/203/138>
25. *Qualitative Analysis of a Small-Signal MOSFET Based Sziklai Pair Amplifier*, **S.N. Shukla**, International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol.7, Issue 12, DOI:10.15662/IJAREEIE.2018.0712013, ISSN-2278-8875, pp.4221-4228, December 2018 (IF-6.392)
26. *Classification of ECG Signals using Cross-Recurrence Quantification Analysis and Probabilistic Neural Network Classifier for Ventricular Tachycardia Patients*, S. Saraswat, G. Srivastava, **S.N. Shukla**, International Journal of Biomedical Engineering and Technology, Vol. 26(2), DOI: <https://doi.org/10.1504/IJBET.2018.089308>, ISSN No: 1752-6426, pp. 141-156, January 23, 2018 (IF-0.60) **(Scopus Indexed)**
27. *Laser Security System Using IOT*, Aditya Kumar, Shipra Saraswat, Saurabh Singh, G. Srivastava, **S.N. Shukla**, Journal of International Academy of Physical Sciences, Vol. 22(2), ISSN 0974-9373, pp. 161-168, June 2018, **(Peer Reviewed)**
URL: <http://www.iaps.org.in/journal/index.php/journaliaps/article/view/133/80>
28. *Real Time System for Efficient Processing of Cardiac Arrhythmias Signals*, S. Saraswat, G. Srivastava, **S.N. Shukla**, Journal of Drug Delivery & Therapeutics, Vol.7(9), DOI: <http://dx.doi.org/10.22270/jddt.v7i6.1518>, ISSN-2250-1177, pp. 7-10, 2017, **(EBSCO Indexed)** (IF-0.675) (UGC List Journal serial number 16693)
29. *Comparative Study of ICI Cancellation Methods in OFDM Systems*, N. Yadav, S.K. Gupta, G. Srivastava, **S.N. Shukla**, Journal of International Academy of Physical Sciences, Vol. 21(3), ISSN 0974-9373, pp. 257-267, 2017 (UGC List Journal Serial No. 48832)
URL: <http://www.iaps.org.in/journal/index.php/journaliaps/article/view/461/354>
30. *Wavelet Transform Based Feature Extraction and Classification of Atrial Fibrillation Arrhythmia*, Biomedical & Pharmacology Journal, S. Saraswat, G. Srivastava, **S.N. Shukla**, DOI: <http://dx.doi.org/10.13005/bpj/1284>, ISSN No. 2456-2610, Vol. 10(4), pp. 1715-1725, 2017 (IF-0.17) **(Scopus Indexed/UGC List Journal serial number 5046)**
31. *Novel Design of 10T Full Adder with 180nm CMOS Technology*, K.K. Verma, **S.N. Shukla**, S.K. Jaiswal, Maharishi Vaish, International Journal of Electronics Engineering Research, Vol.9, Number 9, ISSN-0975-6450, pp.1407-1414, 2017 (IF-2.38) (UGC List Journal serial number 16752), URL: https://www.ripublication.com/ijeer17/ijeerv9n9_03.pdf
32. *Design and Analysis of Phase Frequency Detector using D Flip-Flop for PLL Application*, **S.N. Shukla**, K.K. Verma, S.K. Jaiswal, S. Chaurasiya, International Journal of Electronics Engineering Research, Vol.9, Number 9, ISSN-0975-6450, pp.1389-1395, 2017 (IF-2.38) (UGC List Journal serial number 16752)
URL: https://www.ripublication.com/ijeer17/ijeerv9n9_05.pdf
33. *Malignant Ventricular Ectopy Classification using Wavelet Transformation and Probabilistic Neural Network Classifier*, S. Saraswat, G. Srivastava, **S.N. Shukla**, Indian Journal of Science and Technology, Vol.9 (40), DOI: 10.17485/ijst/2016/v9i40/95486, ISSN-0974-6846, pp.1-5, October 2016, **(Web of Science)** (IF-0.68)
34. *Development of Small-Signal Amplifier using BJT-JFET Hybrid Unit in Sziklai Pair Topology*, **S.N. Shukla**, International Journal of Advanced Research in Electrical, Electronics and

- Instrumentation Engineering, Vol.4, Issue 12, DOI:10.15662/IJAREEIE.2015.0412021, ISSN-2278-8875, pp.9818-9822, 2015 (IF-5.016)
35. *An Experimental Study of the Common Emitter Amplifier using Differential Amplifiers*, R. Singh, **S. N. Shukla**, Pragmaan: Journal of Information Technology, Vol.13, Issue 1, ISSN-0974-5513, pp.14-16, 2015
 36. *Development of Wave Shaping Circuits through PSpice and their Physical Verification*, R. Singh, **S. N. Shukla**, Pragmaan: Journal of Information Technology, Vol.13, Issue 1, ISSN-0974-5513, pp.01-03, 2015
 37. *Qualitative Analysis of Darlington pair Based Modified Small-signal amplifier*, S. N. Shukla, R. Singh, B. Pandey, International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol.4, Issue 4, DOI:10.15662/ijareeie.2015.0404078, ISSN-2278-8875, pp.2181-2186, 2015 (IF-5.016)
 38. *Small-Signal Amplifiers with BJT, FET and MOSFET in Triple Darlington Topology*, **S. N. Shukla**, N. K. Chaudhary, International Journal of Engineering and Advanced Technology, ISSN-2249-8958, Vol.4 No.1, pp.102-106, October 2014 (IF-1.097)
URL: <https://www.ijeat.org/portfolio-item/a3488104114/>
 39. *Small-signal amplifier with JFETs in triple Darlington topology*, **S. N. Shukla**, International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol.3, Issue 9, DOI: 10.15662/ijareeie.2014.0309027, ISSN-2278-8875, p-18812, September 2014 (IF-3.738)
 40. *A New Circuit Model of Small-signal Sziklai pair amplifier*, **S.N. Shukla**, S. Srivastava, International Journal of Applied Physics and Mathematics, Vol.3, No.4, DOI: 10.7763/IJAPM.2013.V3.211, ISSN-2010-363X, pp.231-236, 2013, **(EBSCO Indexed)**
 41. *A New Circuit Model of Small-signal Amplifier using JFETs in Darlington pair Configuration*, **S.N. Shukla**, S. Srivastava, International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol.2, Issue 4, ISSN-2278-8875, p-1554-1560, April 2013 (IF-1.686)
URL: http://www.ijareeie.com/upload/april/56_H_A%20New%20Circuit.pdf
 42. *Development and Qualitative Analysis of a New Circuit Model of Two-stage Small-signal Sziklai pair Amplifier*, **S.N. Shukla**, S. Srivastava, International Journal of Computer Theory and Engineering, Vol.5, No. 4, DOI: 10.7763/IJCTE.2013.V5.772, ISSN-1793-8201, pp.668-672, 2013
 43. *Small-signal Amplifier with MOSFET and BJT in Triple Darlington Configuration*, S. Srivastava, N.K. Chaudhary, **S.N. Shukla**, International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol.2, Issue 2, ISSN-2278-8875, February 2013, p.855-861 (IF-1.686)
URL: http://www.ijareeie.com/upload/february/15_Small-Signal%20Amplifier.pdf
 44. *A New Circuit Model of Small-Signal Amplifier Using MOSFETs in Triple Darlington Topology*, **S. N. Shukla**, S. Srivastava, International Journal of Modeling and Optimization Vol. 3, No. 5, DOI: 10.7763/IJMO.2013.V3.306, ISSN: 2010-3697, pp.394-398, 2013
 45. *Backscattered Imaging of Turbid Media*, N.K. Chaudharya, **S.N. Shukla**, M. Misra, European International Journal of Science and Technology, Vol. 2 No. 1, ISSN: 2304-9693, p.179-182, February 2013
URL: https://ejst.org.uk/images/frontImages/gallery/Vol._2_No._1/17.pdf

46. *Qualitative and Tuning Performance of MOSFET Based Small-Signal Darlington pair Amplifiers*, **S.N. Shukla**, S. Srivastava, International Journal of Enhanced Research in Science Technology & Engineering, Vol.1, Issue 2, DOI: <http://www.doi.org/ste/13.01.08>, ISSN No- 2319-7463, p-1-6, January 2013 (IF-1.252)
47. *Dielectric and Optical Study of Polymer Nematic Liquid Crystal Composite*, S. Manohar, **S.N. Shukla**, V.S. Chandel, J.P. Shukla, R. Manohar, Transactions on Electrical and Electronic Materials, Vol.14, No.3, DOI:10.4313/TEEM.2013.14.3.111, ISSN No- 1229-7607, p-111-115, 2013 (IF-0.312) (**Web of Science/Scopus Indexed**)
48. *Small-signal Amplifier with Three Dissimilar Active Devices in Triple Darlington Topology*, **S.N. Shukla**, S. Srivastava, International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol.1, Issue 6, ISSN-2278-8875, p-502-508, December 2012 (IF-1.686)
URL: http://www.ijareeie.com/upload/december/5_Small-signal%20Amplifier.pdf
49. *A Novel Circuit Model of Small-signal Amplifier Developed by using BJT-JFET-BJT in Triple Darlington Configuration*, **S.N. Shukla**, S. Srivastava, International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol.1, Issue 5, ISSN-2278-8875, p-343-350, November 2012 (IF-1.686)
URL: <http://www.ijareeie.com/upload/november/2-A%20Novel%20Circuit%20Model.pdf>
50. *Qualitative Analysis of Small-Signal Modified Sziklai Pair Amplifier*, B. Pandey, S. Srivastava, S. N. Tiwari, J. Singh, **S. N. Shukla**, Indian Journal of Pure and Applied Physics, ISSN 0975-1041, Vol. 50, April-2012, pp.272-276 (IF-0.852) (**Web of Science**)
URL: <http://nopr.niscpr.res.in/handle/123456789/13784>
51. *Comparative Dielectric and Optical Study of a Pure and Polymer Doped Liquid Crystal Showing Smectic A Phase*, Shashwati Manohar, **S.N. Shukla**, Vishal Singh Chandel, J. P. Shukla, Rajiv Manohar, Journal of Science and Arts, Year 12, No. 3(20), ISSN-1844-9581, pp. 317-322, 2012, (**Web of Science**) (IF-0.245)
URL: http://www.josa.ro/docs/josa_2012_3/b_03_Shashwati_Manohar.pdf
52. *On Demand Realization of Input and Output Resistances of MOSFET Amplifier*, Meena Singh, **S.N. Shukla**, B.P. Singh, International Journal of Computer Sciences, Software Engineering and Electrical Communication Engineering, Vol.3, No.1, ISSN-2229-3175, p-27-32, January-June 2012.
53. *Mathematical Modeling of Electronic Devices and Circuits*, B.P. Singh, Meena Singh, S.K. Roy, **S.N. Shukla**, International Journal of Computer Sciences, Software Engineering and Electrical Communication Engineering, Vol.3, No.1, ISSN-2229-3175, p-19-25, January-June 2012.
54. *Concept of Digital Systems for Square Root*, V.K. Srivastava, S.K. Sharma, **S.N. Shukla**, International Transactions in Applied Sciences, ISSN 0975-3761, Vol.4, No.1, p.51-60, 2012
55. *Complimentary-Symmetry Class-B Push-Pull Amplifier with Improved Efficiency and Reduced Harmonic Distortion*, B. Pandey, S. Srivastava, S.N. Tiwari, J. Singh, **S.N. Shukla**, Journal of Ultra Scientist of Physical Sciences, ISSN 0970-9150, Vol. 23(2)B, p.353-358, 2011 (IF-0.031), URL: <http://www.ultraphysicalsciences.org/paper/622/>
56. *Qualitative Analysis of MOS Based Complementary-Symmetry Class-B Push-Pull Amplifier with Improved Efficiency*, S. Srivastava, B. Pandey, S.N. Tiwari, **S.N. Shukla**, Journal of

- Ultra Scientist of Physical Sciences, ISSN 0970-9150 Vol. 23(3)B, p.703-708, 2011 (IF-0.031), URL: <http://www.ultraphysicalsciences.org/paper/669/>
57. *Cascading of Modified Darlington Pair Amplifier*, S.N. Tiwari, S. Srivastava, B. Pandey, **S.N. Shukla**, Journal of Ultra Scientist of Physical Sciences, ISSN 0970-9150, Vol. 23(1), p.181-188, 2011 (IF-0.031)
 58. *Qualitative analysis of MOS based Darlington Pair Amplifiers*, S. Srivastava, B. Pandey, S.N. Tiwari, J. Singh, **S.N. Shukla**, Bulletin of Pure and Applied Science, ISSN 0970-6569, Vol. 30D (Physics), No.2, p-195, 2011
 59. *Development of small-signal high voltage gain amplifier using compound unit of BJT and MOSFET*, S. Srivastava, B. Pandey, S.N. Tiwari, J. Singh, **S.N. Shukla**, Acta Ciencia Indica, ISSN 0253-732X, Vol.XXXVIIP, No.4, p. 431, 2011
 60. *Implementation of IEEE-754 to Decimal Conversion of Library Components for Floating Point Arithmetic Unit*, S.K. Sharma, V.K. Srivastava, **S.N. Shukla**, International Transactions in Mathematical Sciences & Computer, ISSN 0975-3753, Vol.4, No.2, p-157-164, 2011
 61. *Qualitative Analysis of Small Signal High Voltage Gain Triple Darlington Amplifier*, S.N. Tiwari, S. Srivastava, B. Pandey, **S.N. Shukla**, Bulletin of Pure and Applied Science, ISSN 0970-6569, Vol. 29D, No.1, p-25-32, 2010
 62. *A Wide Band Amplifier Circuit Developed by Modifying Conventional Darlington Pair Amplifier*, S.N. Tiwari, B. Pandey, A.K. Dwivedi, **S.N. Shukla**, Acta Ciencia Indica, ISSN 0253-732X, Vol.XXXVI P, No.3, p. 317, 2010
 63. *Two Discretely Modified Darlington Amplifiers in Cascade*, S.N. Tiwari, A.K. Dwivedi, B. Pandey, **S.N. Shukla**, Acta Ciencia Indica, ISSN 0253-732X, Vol.XXXVI P, No.2, p. 291, 2010
 64. *Qualitative Performance of a Two-Stage Amplifier, Configured by Cascading of Darlington Pair Amplifier with CE Amplifier Circuit*, S.N. Tiwari, S. Srivastava, B. Pandey, **S.N. Shukla**, Journal of Ultra Scientist of Physical Sciences, ISSN 0970-9150, Vol. 22(3), p.493-498, 2010 (IF-0.055), URL: <http://www.ultraphysicalsciences.org/paper/924/>
 65. *Development of Binary to Decimal Conversion of Library Components for IEEE-754 Floating Point Arithmetic Unit*, S.K. Sharma, **S.N. Shukla**, V.K. Srivastava, Journal of Computer and Mathematical Sciences, ISSN 0976-5727, Vol. 1(6), p-636-641, 2010
 66. *Voltage Gain Performance of Two-stage Amplifier, Configured by Coupling Common Emitter and Darlington Pair Amplifier*, S.N. Tiwari, S. Srivastava, B. Pandey, **S.N. Shukla**, Bulletin of Pure and Applied Science, ISSN 0970-6569, Vol. 29D, No.2, p-195-204, 2010
 67. *An Experimental Study on the Life Time of Commercially Available White pc LED for Indian Standards*, R.Singh, A. Chakraborty, **S.N. Shukla**, International Journal of Electronics Engineering, ISSN 0973-7383, Vol.2, Issue.2, p-327, 2010
 68. *Current Conveyor: Novel Universal Active Block*, I.P. Singh, K. Singh, **S.N. Shukla**, SAMRIDDHI-A Journal of Physical Sciences, Engineering and Technology, ISSN 2229-7111, Vol.1, No.1, p-41-48, 2010
 69. *Electrical and Structural Properties of CdTe/ZnTe Hetrojunction Thin Films*, P.K. Pandey, K.S. Upadhyay, **S.N. Shukla**, P.K. Singh, D. Sharma, International Transactions in Applied Sciences, ISSN 0974-7273, Vol.1, No. 4, pp. 483-491, December 2009

70. *Qualitative Analysis of Two Distinct Wide Band Triple Darlington Amplifiers*, S.N. Tiwari, B. Pandey, **S.N. Shukla**, Journal of Ultra Scientist of Physical Sciences, ISSN 0970-9150, Vol 21, No.1, p-117, 2009 (IF-0.043), URL: <http://www.ultraphysicalsciences.org/paper/1083/>
71. *Qualitative Analysis of Small Signal Modified Darlington Pair and Triple Darlington Amplifiers*, S.N. Tiwari, **S.N. Shukla**, Bulletin of Pure and Applied Science, ISSN 0970-6569, Vol. 28D, No.1, p-1-11, 2009
72. *Darlington pair amplifiers in cascade*, S.N. Tiwari, A.K. Dwivedi, **S.N. Shukla**, Journal of Ultra Scientist of Physical Sciences, ISSN 0970-9150, Vol. 21 No.2, p-253-258, 2009 (IF-0.043), URL: <http://www.ultraphysicalsciences.org/paper/1115/>
73. *Qualitative Analysis of Small-Signal Wide-Band Triple Darlington Amplifier*, S.N. Tiwari, **S.N. Shukla**, Acta Cincia Indica, ISSN 0253-732X, Vol.XXXV P, No.4, p. 559, 2009
74. *Development of Small-Signal Amplifiers by Placing BJT and JFET in Darlington Pair Configuration*, S.N. Tiwari, B. Pandey, A.K. Dwivedi, **S.N. Shukla**, Journal of Ultra Scientist of Physical Sciences, ISSN 0970-9150, Vol.21, No.3, p-509-514, 2009 (IF-0.043) URL: <http://www.ultraphysicalsciences.org/paper/1163/>
75. *Implementation of Conversion Process of Library Components for Floating Point Arithmetic Logic Unit*, V.K. Srivastava, S.K. Sharma, **S.N. Shukla**, H. Pandey, Journal of Physical Sciences, ISSN 0975-5519, Vol. 1 (No.-1), p-117-122, 2009
76. *Implementation of N-Bit Adder and Subtractor of Library Components for Arithmetic Unit*, V.K. Srivastava, S.K. Sharma, **S.N. Shukla**, H. Pandey, Journal of Computer and Mathematical Sciences, ISSN 0976-5727, Vol. 1(1), p-21-26, 2009
77. *Structural Properties of CdTe/ZnTe Hetrojunction Thin Films*, P.K. Pandey, K.S. Upadhyaya, **S.N. Shukla**, and P.K. Singh, Journal of Purvanchal Academy of Sciences, ISSN 0972-3498, Vol.15 (Physical Science), pp.51-55, 2009
78. *Electroluminescence in Cu and Mn Activated ZnO Electroluminophors*, **S.N. Shukla**, L.K. Singh, P.K. Singh, R.C. Tiwari, Acta Cincia Indica, ISSN 0253-732X, Vol.XXXIV P, No.1, p. 077, 2008
79. *Rectifying Properties of the Series and Parallel Combination of Like Diodes*, S.N. Tiwari, **S.N. Shukla**, A.K. Dwivedi, Acta Cincia Indica, ISSN 0253-732X, Vol.XXXIV P, No.3, p. 357, 2008
80. *A High Voltage Gain Amplifier Developed By Modifying Conventional Darlington Pair Amplifier Circuit*, S.N. Tiwari, K.K. Dubey, J. Singh, **S.N. Shukla**, Journal of Ultra Scientist of Physical Sciences, ISSN 0970-9150, Vol 20, No.2, p-319, 2008 (IF-0.0376) URL: <http://www.ultraphysicalsciences.org/paper/1306/>
81. *Qualitative Analysis of Small Signal Amplifier Circuits Configured By Coupling of BJTs*, S.N. Tiwari, K.K. Dubey, J. Singh, **S.N. Shukla**, Journal of Current Sciences, ISSN 0972-6101, Vol 12, No.2, p-459-465, 2008
82. *Qualitative Analysis of Modified Darlington Amplifier*, S.N. Tiwari, A.K. Dwivedi, **S.N. Shukla**, Journal of Ultra Scientist of Physical Sciences, ISSN 0970-9150, Vol 20, No.3, p-625, 2008 (IF-0.0376), URL: <http://www.ultraphysicalsciences.org/paper/1349/>
83. *Qualitative Analysis of Small Signal Amplifier Circuits Configured By Coupling of FETs*, S.N. Tiwari, A.K. Dwivedi and **S.N. Shukla**, Journal of Current Sciences, ISSN 0972-6101, Vol 12, No.2, p-741, 2008

84. *Energy Transfer & Sensitization Mechanism in Cu,Mn Activated Electroluminophors (An Introduction)*, R.C. Tiwari, **S.N. Shukla** and L.K. Singh, Science Vision, ISSN 0975-6175, Vol.6, No. 1-2, p.30-33, 2006
85. *Characteristics Studies and Circuit Implementation of the Series and Parallel Combinations of Like Diodes*, S.N. Tiwari, R. Pandey, K.K. Dubey, L.K. Singh and S.N. Shukla, Acta Cincia Indica, ISSN 0253-732X, Vol.XXXI P, No.2, p. 257, 2005
86. *Photoluminescence in Double Band Emitting (ZnS+HgO):Cu,Mn(H) Binary Systems*, **S.N. Shukla**, R.C. Tiwari, P.K. Singh and L.K. Singh, Journal of Ravi Shankar University, ISSN 0970-5910, Vol.18, No. B(Science), p. 49-55, 2005
URL: <https://jru-b.com/AbstractView.aspx?PID=2005-18-1-6>
87. *Comparison of Electroluminescence in ZnO-CdO and ZnO-CdS Base Binary Systems*, **S.N. Shukla**, P.K. Singh, R.C. Tiwari and L.K. Singh, Acta Cincia Indica, ISSN 0253-732X, Vol.XXX P, No.3, p. 271, 2004
88. *Simulation and Analysis of an Improved Astable Multivibrator Through PSPICE*, R. Pandey, **S.N. Shukla** and L.K. Singh, Acta Cincia Indica, ISSN 0253-732X, Vol.XXX P, No.3, p. 309, 2004
89. *Electroluminescent Behaviour of ZnSe:Mn Depending Upon Preparatory Conditions*, **S.N. Shukla**, R.C. Tiwari and L.K. Singh, Journal of Purvanchal Academy of Sciences, ISSN 0972-3498, Vol.6, p.174-182, 2000
90. *Electroluminescent Behaviour of Double Band Emitting (ZnS+HgO):Cu,Mn System*, R.C. Tiwari, **S.N. Shukla**, S.K. Srivastava and L.K. Singh, Acta Cincia Indica, ISSN 0253-732X, Vol.XXII P, No.4, p.127, 1996
91. *Comparison of Electroluminescent Nature of Certain ZnO and ZnS Base Binary Systems*, **S.N. Shukla**, R.C. Tiwari and L.K. Singh, National Academy Science Letters, ISSN 0250-541X, Vol.16, No.2, p.67, 1993 (IF-0.078)
92. *Radiation Controlled Electroluminescence in Triple Band Emitting Electroluminescent Materials*, G. Singh, R.C. Tiwari, **S.N. Shukla** and L.K. Singh, Indian Journal of Physics, ISSN 0973-1458, 66A(3), p.289, 1992 (IF-0.072)
93. *Temperature Dependent Electroluminescent Studies of Double Band Emitting ZnO:Cu,Mn(H) Electroluminophors*, **S.N. Shukla**, R.C. Tiwari, S.K. Srivastava and L.K. Singh, Journal of Purvanchal Academy of Sciences, ISSN 0972-3498, Vol.3, p.85, 1992
94. *Photoluminescence of Doubly Activated ZnS Phosphors*, **S.N. Shukla**, G. Singh and L.K. Singh, Journal of Ravi Shankar University (Part-B: Science), ISSN 0970-5910, Vol. 5(1), pp.177-184, 1992, URL: <https://jru-b.com/AbstractView.aspx?PID=1992-5-1-7>
95. *Radiation Controlled Electroluminescence in Double Band Emitting ZnO-Base Electroluminophors*, **S.N. Shukla**, R.C. Tiwari and L.K. Singh, Journal of Ravi Shankar University (Part-B: Science), ISSN 0970-5910, Vol. 5(1), pp.185-191, 1992
URL: <https://jru-b.com/HTMLPaper.aspx?Journal=Journal%20of%20Ravishankar%20University;PID=1992-5-1-8>

(B) PAPERS PUBLISHED IN CONFERENCE PROCEEDINGS (18)

96. *Electronically Tunable Low Power Cascaded OTA-C Fifth Order Notch Filter for Front End ECG Processing System*, A. Dixit, G. Srivastava, A. Kumar, **S.N. Shukla**, "Proceedings of the 5th International Conference on Smart Electronics and Communication (ICOSEC 2024)" organized by Kongunadu College of Engineering and Technology, Trichy, India on

September 18-20, 2024, ISBN: 979-8-3315-0439-7, pp.1981-1987, September 2024 (**IEEE Conference**)

97. *FPGA-based Hardware Classifier for Diabetic Sensorimotor Polyneuropathy Severity Assessment*, S.K. Pandey, G. Srivastava, Mamun B. Reaz, Sawal H. Md Ali, Edi Kurniawan, R. G. Thangarajoo, G. R. Mishra, **S.N. Shukla**, CEUR Workshop proceedings, Proceedings of AISD 2023 (First International Workshop on Artificial Intelligence: Empowering Sustainable Development, co-located with First International Conference on Artificial Intelligence: Towards Sustainable Intelligence, Pune, India, September 4-5, 2023), ISSN 1613-0073, Vol. 3619, pp. 33-41, https://ceur-ws.org/Vol-3619/AISD_Paper_4.pdf
98. *Novel JFET Complementary Sziklai pair based Small Signal Amplifiers for Wireless Communication Receivers*, S.S. Arshad, **S.N. Shukla**, G. Srivastava, P. Soni, S.N. Singh, 9th IEEE International Conference on Smart Instrumentation, Measurement and Applications (ICSIMA) Kuala Lumpur, Malaysia, 2023, pp. 331-336, DOI: 10.1109/ICSIMA59853.2023.10373535 (**Publisher: IEEE-Xplore**) (**Scopus Indexed**)
99. *Low Power Low Pass Operational Transconductance Amplifier based RC Active Filter*, A. Dixit, **S.N. Shukla**, G. Srivastava, Edi Kurniawan, Anil Kumar, Jalu Ahmad Prakosa, 9th IEEE International Conference on Smart Instrumentation, Measurement and Applications (ICSIMA) Kuala Lumpur, Malaysia, 2023, pp. 337-342, DOI: 10.1109/ICSIMA59853.2023.10373552. (**Publisher: IEEE-Xplore**) (**Scopus Indexed**)
100. *Study of Novel Small-Signal JFET Amplifiers in Sziklai pair Topology*, **S. N. Shukla**, S. Shamroz Arshad, G. Srivastava, A. K. Sharma, S. K. Pandey, S. N. Singh and P. Soni, Proceedings of 10th International Conference on Signal Processing and Integrated Networks (SPIN-2023), Amity University, Noida, March 23-24, 2023, pp. 93-98, May 9, 2023, DOI: 10.1109/SPIN57001.2023.10117320 (**Publisher: IEEE-Xplore**) (**Scopus Indexed**)
101. *Classification of ECG Signals Related to Paroxysmal Atrial Fibrillation*, S. Saraswat, G. Srivastava, **S.N. Shukla**, Proceedings SSIC-2017, First International Conference on Smart System, Innovation and Computing, Manipal University, Jaipur, 15-16 April 2017, ISBN 978-981-10-5828-8, DOI <https://doi.org/10.1007/978-981-10-5828-8>, pp. 477-485 (Publisher: Springer, Singapore) (Publication date: January 9, 2018)
102. *New Small-Signal Amplifying System with Sziklai Pairs in Triple-Transistor Topology*, **S.N. Shukla**, Proceedings IEEE-ICAESE-2016, IEEE International Conference on Advances in Electrical, Electronic and System Engineering, November 14-16, 2016, Kuala Lumpur, Malaysia ISBN 978-1-5090-2888-7, pp.480-485, IEEE-Xplore, November 2016 (**Web of Science**)
103. *Design and Analysis of Low Power CMOS ECG Amplifier*, K.K. Verma, **S. N. Shukla**, S. K. Jaisawal, Kumkum Verma, Proceedings ICETEESES-16 Vol.2, International Conference on Emerging Trends in Electrical, Electronics & Sustainable Energy Systems, March 11-12, 2016, K.N.I.T., Sultanpur, UP, India, DOI: 10.1109/ICETEESES.2016.7581404, ISBN 978-1-5090-2118-5, pp.334-336, March 2016 (**Web of Science**)
104. *Decomposition of ECG Signals using Discrete Wavelet Transform for Wolff Parkinson White Syndrome Patients*, S. Saraswat, G. Srivastava, **S.N. Shukla**, Proceedings IEEE-ICMETE-2016, September) IEEE International Conference on Micro-Electronics and Communication Engineering, SRM University, Delhi-NCR Campus, Ghaziabad, 22-23

- September 2016, ISBN 978-1-5090-3411-6, DOI 10.1109/ICMETE.2016.79, pp. 361-365
(**Web of Science**)
105. *ECG Signal Analysis Using Artificial Intelligence Techniques: A Review*, S. Saraswat, G. Srivastava, **S.N. Shukla**, Proceedings IGLRC-2016, International Global Leadership Research Conference, Amity Business School, Noida, 22-23 January 2016, pp. 25-26
(**EBSCO Indexed**)
 106. *Novel Circuit of Small-signal Amplifier with BJT-MOSFET Hybrid Unit in Sziklai Pair Topology*, **S. N. Shukla**, Proceedings IEEE RADIO 2015, IEEE Radio and Antenna Days of the Indian Ocean, September 21-24, 2015, Belle Mare, MAURITIUS, DOI: 10.1109/RADIO.2015.7323394, ISBN 978-9-9903-7339-4, pp.1-2, IEEE-Xplore, 2015
(**Web of Science**)
 107. *Review: Comparison of QRS Detection Algorithms*, S. Saraswat, G. Srivastava, **S.N. Shukla**, Proceedings IEEE-ICCCA-2015, IEEE-International Conference on Computing, Communication and Automation, May 15-16, 2015, DOI: 10.1109/CCAA.2015.7148443, ISBN 978-1-4799-8890-7, pp. 354-359, IEEE-Xplore, May 2015 (**Web of Science**)
 108. *Two-Stage Small-Signal Amplifier with Darlington and Sziklai pairs*, **S.N. Shukla**, B. Pandey, Proceedings IEEE-ICSE-2014, 11th IEEE International Conference on Semiconductor Electronics, August 27-29, 2014, Kuala Lumpur, DOI: 10.1109/SMELEC.2014.6920783, ISBN 978-1-4799-5759-0), pp.13-16, IEEE-Xplore, 2014
(**Web of Science/Scopus Indexed**)
 109. *Qualitative Analysis of Small-signal Modified Darlington pair Amplifiers*, B. Pandey, **S.N. Shukla**, Proceedings ICSP-2014, National Conference on Integrated Circuits and Signal Processing, Amity University, Lucknow Campus, Lucknow, September 25-26, 2014, ISBN 978-81-927441-3-1, pp.11-15
 110. *New Circuit Model of Small-signal Amplifier Developed by using MOSFETs in Triple Darlington Configuration*, **S.N. Shukla**, S. Srivastava, Rajamangala University of Technology, Proc.- The 15th International Conference of International Academy of Physical Sciences, December 9-13, 2012, Pathumthani, Thailand, 2014, pp.125-132, Link-<http://www.repository.rmutt.ac.th/dspace/handle/123456789/1321>
 111. *Qualitative Study of a New Circuit Model of Small-signal Amplifier using Sziklai pair in Compound Configuration*, **S.N. Shukla**, B. Pandey, S. Srivastava, Proceedings IEEE-ICSE-2012, 10th IEEE International Conference on Semiconductor Electronics, September 19-21, 2012, Kuala Lumpur, DOI: 10.1109/SMElec.2012.6417209, ISBN 978-1-4673-2396-3, pp.563-569, IEEE-Xplore, 2012 (**Web of Science**)
 112. *New Circuit Models of Complementary-Symmetry Class-AB and Class-B Push-Pull Amplifiers*, **S.N. Shukla**, B. Pandey, S. Srivastava, Proceedings IEEE-ICSE-2012, 10th IEEE International Conference on Semiconductor Electronics, September 19-21, 2012, Kuala Lumpur, Malaysia, DOI: 10.1109/SMElec..2012.6417203, ISBN 978-1-4673-2396-3, pp.538-542, IEEE-Xplore, 2012 (**Web of Science**)
 113. *Development of Library Components for Floating Point Processor*, V.K. Srivastava, **S.N. Shukla**, S.K. Sharma, Proceedings of the National Seminar on Wireless Communications and Networks, ISBN 978-81-7880-603-7, 24-25 March 2012, Shri Mata Vaishno Devi University, Katra, J & K, p-1-5, 2012

(C) PAPERS PUBLISHED AS BOOK CHAPTERS (09)

114. *Study of Low-noise Wide-band Tuned Sziklai pair Small-signal Amplifier*, **S.N. Shukla**, G. Srivastava and S.S. Arshad, "Chapter-01 in Book - Research Trends and Challenges in Physical Sciences Vol. 1", Print ISBN: 978-93-91595-88-3, eBook ISBN: 978-93-91595-96-8, pp. 01-16, DOI: 10.9734/bpi/rtcps/v1/4064F, (Publisher: BP International) August 24,2021
115. *Study of Darlington pair Amplifier with Matched N-Channel MOSFETs*, **S.N. Shukla**, "Chapter-12 in Book - New Approaches in Engineering Research Vol. 9" Print ISBN: 978-93-91595-18-0, eBook ISBN: 978-93-91595-26-5, pp. 141-153, DOI: 10.9734/bpi/naer/v9/11460D, (Publisher: BP International) August 03,2021
116. *Low-frequency Small-signal Amplifier with BJT-JFET Hybrid Unit in Sziklai Pair Topology*, **S.N. Shukla**, P. Soni and G. Srivastava, "Chapter-10 in Book - Advanced Aspects of Engineering Research Vol. 13" Print ISBN: 978-93-91215-48-4, eBook ISBN: 978-93-91215-49-1, pp. 108-122, DOI: 10.9734/bpi/aaer/v13/2268F, (Publisher: BP International) May 12,2021
117. *Review of Ultra-Low- Power CMOS Amplifier for Bio-electronic Sensor Interface*, G. Srivastava, A. Dixit, A. Kumar, **S.N. Shukla**, "Book – Smart Innovations in Communication and Computational Sciences", ISBN 978-981-15-5345-5, https://doi.org/10.1007/978-981-15-5345-5_24, pp 263-272 (Publisher: Springer, Singapore), August 2, 2020
118. *Brightness Wave Forms in Triple Band Emitting ZnS:Cu,Mn(H) Electroluminophors*, S.K. Srivastava, G. Singh, **S.N. Shukla**, L.K. Singh, "Book - Luminescence and Its Applications", ISBN 81-7023-520-0, p.298 (Publisher: Allied Publishers), 1996
119. *Electroluminescence in Double Band Emitting (ZnO+ZnSe) Binary System*, **S.N. Shukla**, S.K. Srivastava, L.K. Singh, "Book - Luminescence and Its Applications", ISSN-0971-6905, Proceedings of the National Seminar held at Govt. PG Science College, Bilaspur, MP, January 23-25, 1994, (Publisher: Luminescence Society of India), p.108, 1995
120. *Energy Transfer in Different Colour Centres of Thermally Stimulated Triple Band Emitting Electroluminophors*, **S.N. Shukla**, L.K. Singh, "Book - Thermoluminescence and Its Applications", ISBN 0-07-462275-7, (Publisher: Tata McGraw Hill), p.288, 1992
121. *Sabka Saath, Sabka Vikas – Bharatiya Drishtikon*, K.K. Mishra, **S.N. Shukla**, "Book - Samajik Samrasta", ISBN 978-93-88003-01-8, (Publisher: Dr. RamManohar Lohia Avadh University) p.35-39, 2018
122. *Rashtriy Shiksha Niti 2020: Ek Vimarsh*, **S.N. Shukla**, "Book – Pragya Parab", ISBN 978-93-94549-97-5, (Publisher: Navbodh Prakashan, Chhattisgarh) p.18-32, 2024

GENERAL ARTICLES PUBLISHED IN NEWS PAPER / MAGAZINE (18)

1. *No Puja, He has Write Attitude to Ram*, Hindustan Times (Lucknow Edition), December 17, 2009, Page-1 (front page) URL: <https://www.hindustantimes.com/india/no-puja-he-has-write-attitude-to-ram/story-iGShpPtFh4213PCdeiOoXM.html>
2. *Sita Ram Naam Bank credits devotees' sacredness*, Sachchidanand Shukla, Hindustan Times (Lucknow Edition), April 06, 2010, page-04
3. *Ayodhya verdict based on ASI report: Archaeologist*, Sachchidanand Shukla, Hindustan Times (Lucknow Edition), October 28, 2010, page-04
URL: <https://www.hindustantimes.com/lucknow/ayodhya-verdict-based-on-asi-report-archaeologist/story-emN4IaooVcCRL9YmywuTBI.html>

4. *Eight wonders of Ayodhya*, Sachchidanand Shukla, Hindustan Times (Lucknow Edition), May 24, 2011, page-03
5. *Symbol of grandeur lost to ravages of time*, Sachchidanand Shukla, Hindustan Times (Lucknow Edition), May 25, 2011, page-03
6. *Centre of devotion has seen ups and downs*, Sachchidanand Shukla, Hindustan Times (Lucknow Edition), May 26, 2011, page-03
7. *Site of Vishnu's secret tapasya*, Sachchidanand Shukla, Hindustan Times (Lucknow Edition), May 27, 2011, page-03
8. *The two avatars of one shrine*, Sachchidanand Shukla, Hindustan Times (Lucknow Edition), May 28, 2011, page-03
9. *The final resting place of Dasharath*, Sachchidanand Shukla, Hindustan Times (Lucknow Edition), May 29, 2011, page-03
10. *Prime pilgrimage spots of ancient times*, Sachchidanand Shukla, Hindustan Times (Lucknow Edition), May 30, 2011, page-03
11. *A 'Prayatr' in the Right Direction*, Sachchidanand Shukla, Hindustan Times (Lucknow Edition), January 01, 2012, page-02
12. *Man on Mission Education*, Sachchidanand Shukla, Hindustan Times (Delhi Edition), May 15, 2012, Page-4, URL: <https://www.hindustantimes.com/india/man-on-mission-education/story-41UNrZo9G2XHmXRE0SrOBM.html>
13. *Where Hope 'Sparkles'*, Sachchidanand Shukla, Hindustan Times (Lucknow Edition), May 24, 2012, page-02
14. *A Class act in UP's Kutiya*, Sachchidanand Shukla, Hindustan Times (Delhi Edition), August 02, 2012, Page-2, URL: <https://www.hindustantimes.com/india/a-class-act-in-up-s-kutiya/story-OmvlHAFU66MYx7pPxjz16K.html>
15. *Novel Experiment with Special Kids Yields Impressive Results*, Sachchidanand Shukla, Hindustan Times (Lucknow Edition), May 14, 2015, Page-02, URL: <https://www.pressreader.com/india/hindustan-times-lucknow/20150514/281779922701458>
16. *Rolex watch, Azad Hind Fauj uniform among Gumnami Baba's possessions*, Sachchidanand Shukla, Hindustan Times (Delhi Edition), February 27, 2016
URL: <https://www.hindustantimes.com/india/rolex-watch-azad-hind-fauj-uniform-among-gumnami-baba-s-posessions/story-cxBEPVaiufuSVvUnYfEYmN.html>
17. *INA Uniform found in Belongings of 'Netaji Seer'*, Sachchidanand Shukla, Hindustan Times (Delhi Edition), February 28, 2016, Page-1 (front page)
18. *Ayodhya Ready for Record Glow*, Sachchidanand Shukla, Hindustan Times (Lucknow Edition), October 16, 2017, Page-05
URL: <https://www.hindustantimes.com/lucknow/grand-diwali-celebrations-ayodhya-ready-for-record-glow/story-lz9iadMWMHNTknif8wegzl.html>